

JSS Mahavidyapeetha JSS Academy of Technical Education, Noida Department of Electronics & Communication Engineering.



FACULTY PROFILE

1. Personal Details

NAME	Dr. CHANDRA SHANKAR	
DEPARTMENT	ELECTRONICS & COMMUNICATION	
	ENGINEERING	Lange For
DESIGNATION	ASSISTANT PROFESSOR	
PHONE	8800959255	
EMAIL ID	chandrashankar@jssaten.ac.in	
Date of Joining	21-04-2011	
(JSSATEN)		

2. Experience

3. Qualifications

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
B.E.	Electronics and Communication Engineering.	2004	GLA Institute ofTechnology and Management Mathura U.P.	U. P. Tech University Lucknow
M.Tech.	VLSI Design	2009	C-DAC Mohali	Punjab Technical University Jalandhar (Punjab)
Ph.D.	ECE	2018	Jaypee Institute ofInformation Technology NOIDA	Jaypee Institute of Information Technology NOIDA
Post Doc.	-	-	-	-

4. Research & Publications

Papers Published in Web of Science indexed Journals	International:05	National: 00		
Papers Published in SCOPUS indexed Journals	International:04	National: 00		
Papers Published in other Journals	International:00	National: 02		
Papers Presented in Conferences / Symposium	International:06	National:03		
		01		
Books / Book chapters Published	Name of the book: Lecture Notes in Electrical Engineering Publisher: Springer Nature Singapur Pte.Ltd.2019, ISBN: 978- 981-13-2552-6, ISSN:1876-1119 Year of Publication: 2019			

5. Research Guidance

PhD Guide? Give field	Yes/Analog Signal	JSS Science and Technology
& University	Processing	University, Mysuru.
PhDs / Projects Guided	Ph.D. Awarded: 0 Guiding: 01	Projects at Master's Level: 0 Projects at Bachelor's Level: 15

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	-	-	-	-

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
1	Design of smart wireless charging system for electric vehicles using WPT Technology	Published

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	•	•	-	-

7. Awards Received

Awards "Best	NT:1
Paper	Nil
Award"	

8. Publications

i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publishe r
1	SIFO— VM/TIM universal biquad filter using single DVCCTA with fully CMOS realization	C. Shankar, S. V. Singh, R.Imam	Analog Integrated Circuits and Signal Processing	Published on line, 2021	WOS / Scopus / Both (SCIE)	1.34	SPRING ER Nature
2.	WSN and IoT based Smart City model using the MQTT protocol	Vishal kumar, Gayatri Sakya, Chandra Shankar	Journal of discrete mathematical science and cryptography	vol.22, issue 8, pp. 1423- 1434, 2019	WOS / Scopus / Both	0.77	Taylor & Francis
3.	A new TIM biquad filter employing single DVCCTA	S.V. Singh, C. Shankar	Journal of Electrical Systems	Vol. 15(2), pp. 249- 263, 2019, ISSN 1112-5209	WOS / Scopus / Both	0.87	Engineeri ng and Scientific Research Groups
4.	High input impedance single input five output transadmittance mode biquad	C. Shankar, S. V. Singh	Indian Journal of Pure and AppliedPhysics	vol.57, pp. 52-62, 2019, P. ISSN 0019-5596	WOS / Scopus / Both (SCIE)	0.923	CSIR- NISCAI R

	filter using						
	DVCCTA						
5.	Fully Integrated Multifunction Trans- impedance mode Biquad Filter	S.V. Singh, C. Shankar	Journal of Engineering Science and Technology	vol.13, no.1, pp. 280-294, 2018, p- ISSN: 1823- 4690.	WOS / Scopus / Both	0.79	Taylor's Universit y, Malaysia
6.	Electronically tunable current mode biquad filter based on single VDTA and grounded passive elements	C. Shankar, S. V. Singh	International Journal of Engineering and Technology (IJET)	vol.9, no.2, pp-271- 279, ISSN: 0975- 4024, 2017	UGC	-	Engg Journals Publicati ons
7.	Single Active Element Based Three input Single output Trans- admittance mode Biquad Universal Filter	S.V. Singh, C. Shankar	International Journal of Engineering and Technology (IJET)	vol. 8, no. 4, pp.1671- 1678, ISSN: 0975- 4024, Aug-Sep 2016	Scopus	-	Engg Journals Publicati ons
8.	Single VDTA Based Multifunction Trans- admittance mode Biquad Filter	C. Shankar, S. V. Singh	International Journal of Engineering and Technology (IJET)	vol.7, no. 6, pp.2180- 2188, Dec 2015-jan 2016,e.ISS N:0975- 4024	Scopus	-	Engg Journals Publicati ons
9.	A New Trans- admittance Mode Biquad Filter using MO-VDTA	C. Shankar, S. V. Singh	WSEAS Transactions on Circuits and Systems	vol.14, pp.8-18, 2015, E- ISSN: 2224- 266X	Scopus	-	WSEAS

ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1.	Stability and Bandwidth Enhancement of Two stageOp-Amp usingNegative Capacitance Generation	C. Shankar, M. Kaur	International Journal of Micro and Nano Electronics, Circuits and Systems	Vol. 2(2), pp. 113- 118"2010, ISSN:0975- 4768	-	-	Serial Publication Pvt. Ltd

iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volume No. Issue No. Year	WOS / Scopu s	Impact Factor	Publisher
1	High input impedance VM biquad filter using single active element	C. Shankar, S. V. Singh, V.A.Tikkiwal	6 th International Co nference on Signal Processing and Communication (ICSC 2020)	5 th - 7 th March, 2020	Scopu s	-	IEEE
2	DVCCTA based high input impedance VIVO Biquad filter	C. Shankar, S. V. Singh, G. Sakya	in proc of International Conference on Computing, Power and Communication Technologies (GUCON)	27-28 th Sept. 2019	Scopu s	-	IEEE
3	DVCCCTA based high input impedance VM biquad filter by the use of minimum number of active block and all grounded passive elements	C. Shankar, et.al	in Proc. of 6th International Conference on Signal Processing and Integrated Networks (SPIN)	7-8 March 2019, pp.1106- 1111, ISBN: 978- 1-7281- 1381-4	SCI	-	IEEE

4	SITO Type Voltage Mode Biquad Filter Based on 5Single VDTA	C. Shankar, et.al,	in Proc. of Advances in Signal Processing and Communication (ICSC -2018)	21-23 March 2018, pp.261- 270, ISBN- 10: 981132552	SCOP US	-	SPRINGE R
5	Three Input Single Output Current mode Biquad Filter using Single VDTA	C. Shankar , et.al	in Proc.of ICSC- 2016,	26-28 Dec 2016, pp. 386-391. ISBN: 978- 1-5090- 2684-5	Scopu s	-	IEEE
6	A low voltage operable VDTA based biquad filter realizing band pass and high pass filtering functions in transadmittancemode	C. Shankar, S. V. Singh	in Proc. of ICCCA- 2015	15-16 May, 2015, pp.1288- 1293	SCI	-	IEEE

iv. Workshops/Conferences Attended

Sl. No.	Name of the workshop / Conference	Organizer	Date
1	IEEE conference (SPIN-2019)	AMITY University	07-Mar-2019 to 08-
1	TELE conference (SI IIV-2017)	Noida	Mar-2019
2	FDP Analog circuits and	Texas Instruments in	18-Aug-2018 to 20-
2	application	ABESIT Ghaziabad	Aug-2018
3	FDP on Entreprenuer Development Programme	Sponssored by NSTEBD,Dept. of Science andTechnology conducted byNational Institute forEntreprenuership andsmall businessDevelopment (NIESBUD),Gautam Budha Nagar U.P	28-Mar-2018 to 30-Mar- 2018
4	Presented conference paper in	Springer at JIIT	21-Mar-2018 to 23-Mar-
	Springer conference(ICSC-2018)	Noida	2018

5	Presented conference paper in IEEE Conference, (ICSC-2016)	IEEE at JIIT Noida	26-Dec-2016 to 28-Dec- 2016
6	Attended 2 week FDPEntrepreneurshipDevelopment Program	Sponsored by NSTEBD,Dept. of Science andTechnology implemented by Entrepreneurship development Institute ofIndia at JSSATEN Noida	05-Dec-2016 to 17-Dec- 2016
7	MATLAB and its Hardware Interface	Sponsored by ICT (MHRD) at JSSATE Noida	11-Jan-2016 to 15-Jan- 2016
8	IEEE Conference, (ICCCA-2015)	Sponssored by IEEE atGalgotia UniversityGreater Noida	15-May-2015 to 16- May-2015
9	Faculty Development program under TEQIP-II on Recent Advances and challenges in Power & Energy for sustainable growth.	Sponsered by TEQIP-II atDTU Delhi	01-Jun-2015 to 05-Jun- 2015
10	NBA workshop and certify as NBAStakeholder•	National Board of Accreditation at JSSATENoida	21-Feb-2013 to 23-Feb- 2013
11	Attended a 2 week workshop on AnalogElectronics.	sponsored by ICT (MHRD)at MGMCET Noida	04-Jun-2013 to 14-Jun- 2013
12	Presented paper in 6th International conferenceon signal processing and communication (ICSC2020)	IEEE at JIIT Noida	05-Mar-2020 to 07-Mar- 2020
13	Presented paper in IEEE sponsored internationalConference on Computing, Power andCommunication Technologies (GUCON)	IEEE at GalgotiaUniversity Greater Noida	27-Sep-2019 to 28-Sep- 2019

v. Workshops / Conference (Organized)

Sl. No.	Name of the workshop / Conference	Organized by	Date	Role
1	Dr. APJ Abdul Kalam Technical University Zonal Level Sport Fest-18	Dr. APJ Abdul Kalam Technical University	25 th to 27 th Oct 2018	Convener
2	INSPIRE science Camp -2016	DST and MST GOI, at JSSATE	26 th to 30 th Dec 2016	member of organizing

		Noida campus		committee
		MTU and		
	T d u-did DTDEE	Schneider	23 rd to 24 th	member of
3	Two days national seminar RTDEE- 2013	Electrical	Aug 2013	organizing
	2013	Infrastructure	Aug 2013	committee
		Ltd. Noida		
	One week Faculty Development		5 th Sept to	Member of
4	Program on Analog /Embedded	JSSATE Noida	10 th Sept	organizing
	Technologies		2016	committee
	One-week International Webinar on		02 nd July	
5	Emerging trend in electronics,	JSSATE Noida	$2020 \text{ to } 7^{\text{th}}$	Faculty
	Communication, and Information	JOSATE NOIGA	July 2020	Coordinator
	Technology		July 2020	

vi. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

Sl. No.	Name of the workshop / Conference	Organizer	Date
-	•	-	-

9. Details of NPTEL / COURSERA courses completed

Sl. No.	Name of the subject	Organized by	Date of completion / Award	Grade / Marks
1	Introduction to Internet of Things	NPTEL	Sept. 2021	ELITE

10. Membership of Professional Bodies:

Nil

11. Any other information you will like to share about your professional experience